Reviewed by: Andrew Newman

Module Name:Tx/Rx FIFO

Jira Ticket:[CAN2-18]

**CAN Controller Requirements Checklist Rev.2**

1. Is requirements document written clearly and grammatically correct?

Yes [x] No [ ]

Comments:

1. Does requirements document define all functions to be performed by the module?

Yes [x] No [ ]

Comments:

1. Does requirements document include all inputs and outputs to the module along with their corresponding accuracy, range of values, frequency and format?

Yes [ ] No [x]

Comments: FIFO\_R\_03 and FIFO\_W\_03 still need fixing. Suggest adding a second requirement for each that goes something along the lines of: “Module shall set 'o\_empty' logic high when ‘w\_ptr’ is equal ‘r\_ptr’ + 1 ”.

1. Is each requirement unique?

Yes [x] No [ ]

Comments:

1. Does requirements document comply with system requirements and traceability?

Yes [x] No [ ]

Comments:

1. Is terminology consistent with Requirements Standards?

Yes [x] No [ ]

Comments:

1. Is it possible to implement all requirements?

Yes [x] No [ ]

Comments:

1. Is each requirement testable or verifiable?

Yes [x] No [ ]

Comments: